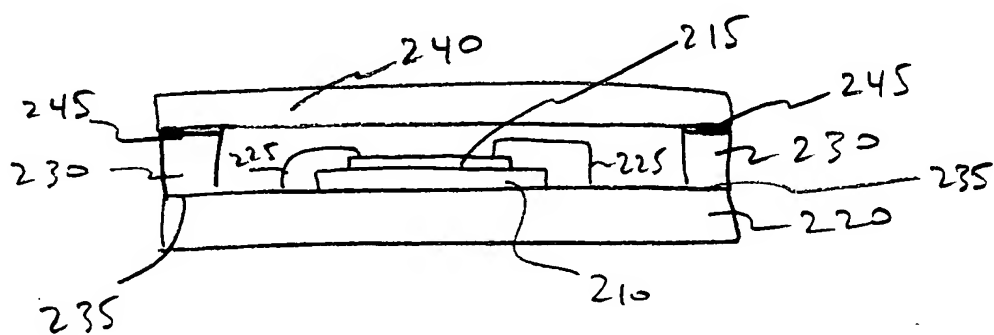


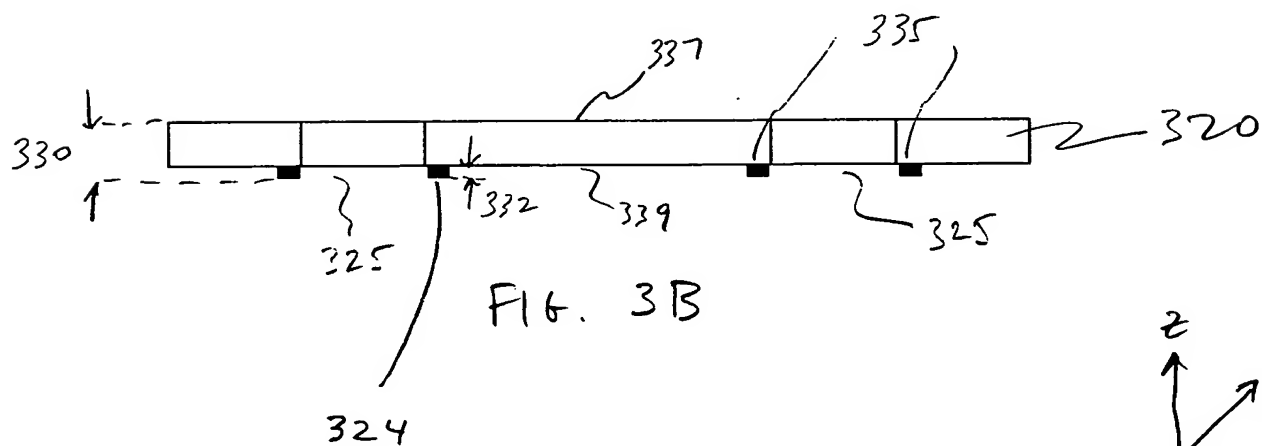
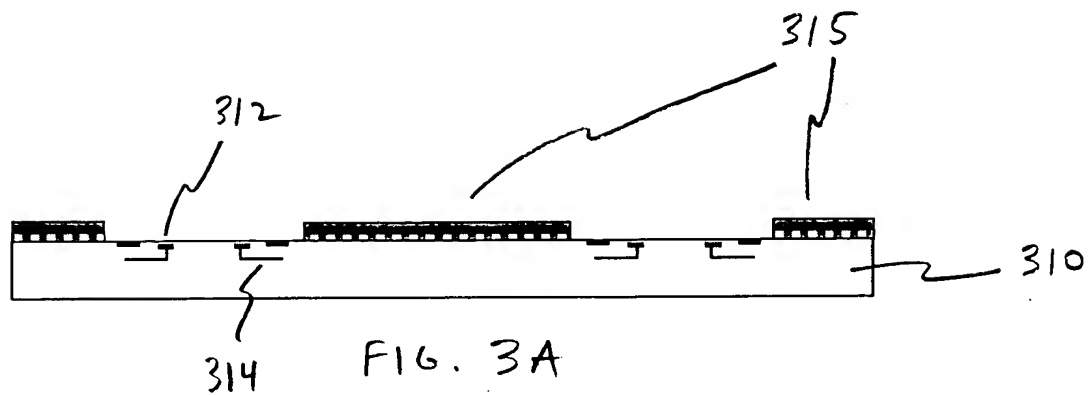
(Prior Art)

FIG. 1



(Prior Art)

FIG. 2



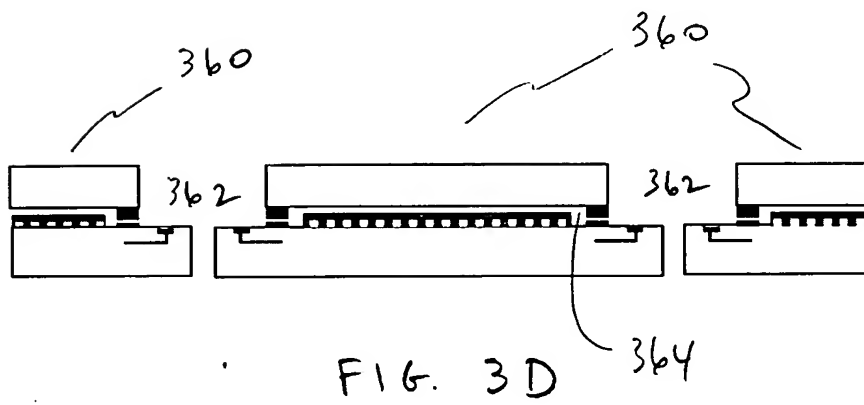
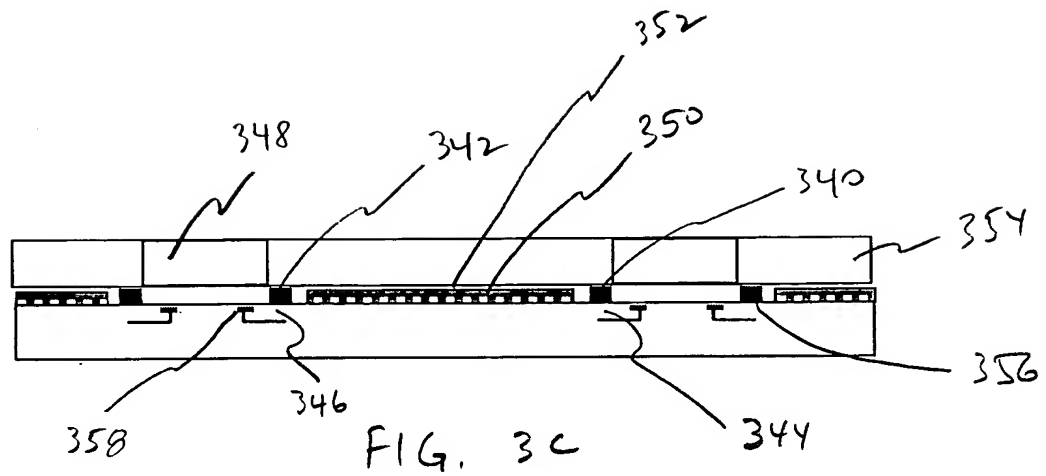


FIG. 4A

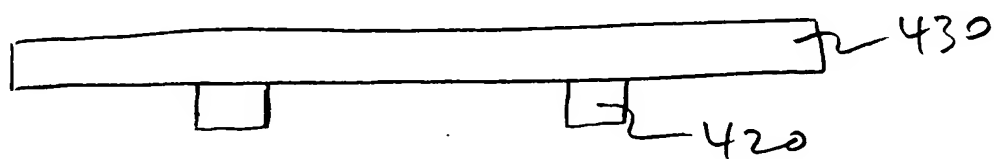
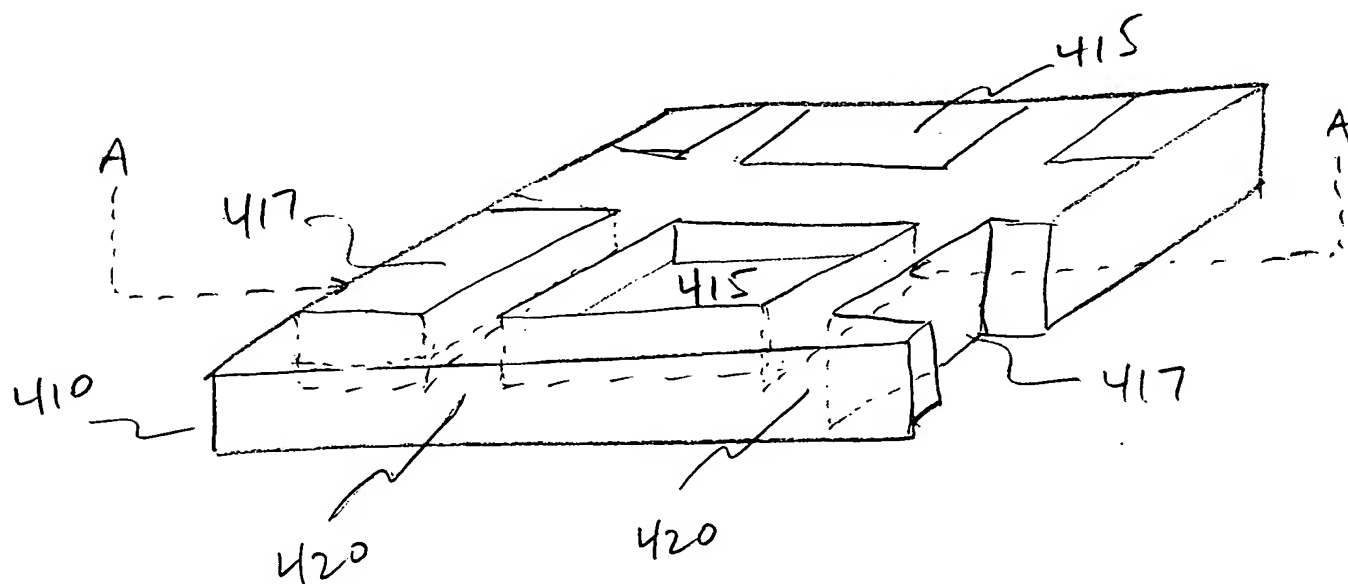
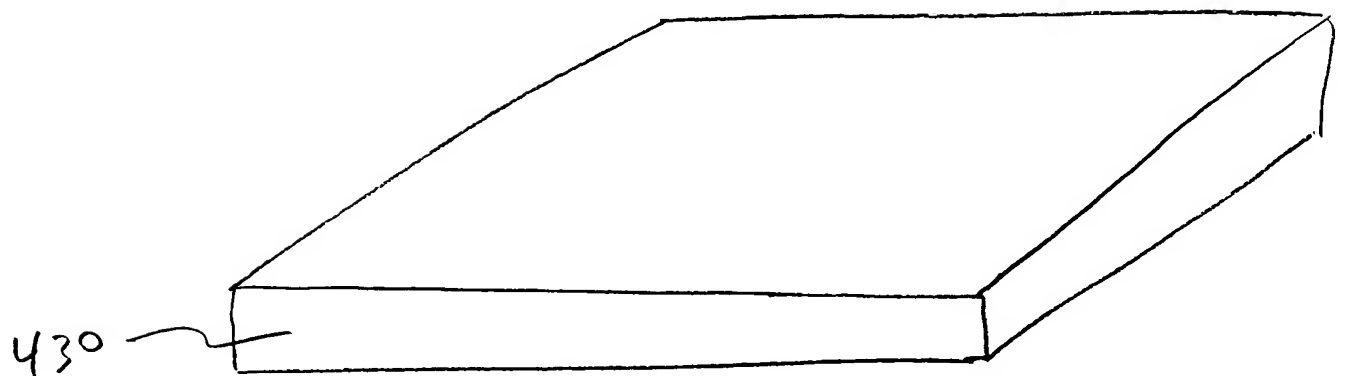
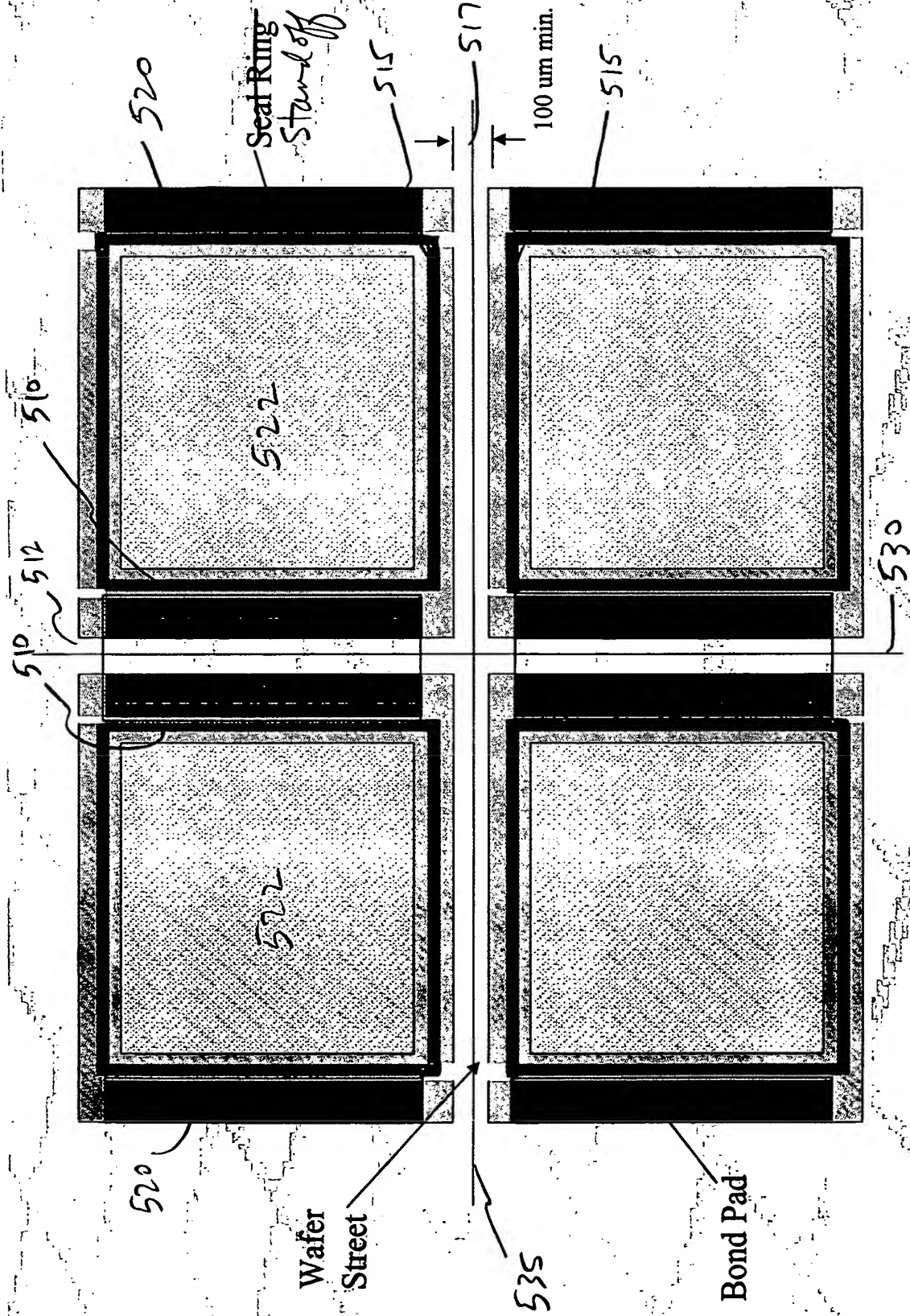


FIG. 4B

FIG. 5A



This slide shows top view of process step 3, 4, and 5.  
Preformed slits in fused silica wafer to expose the bonding area

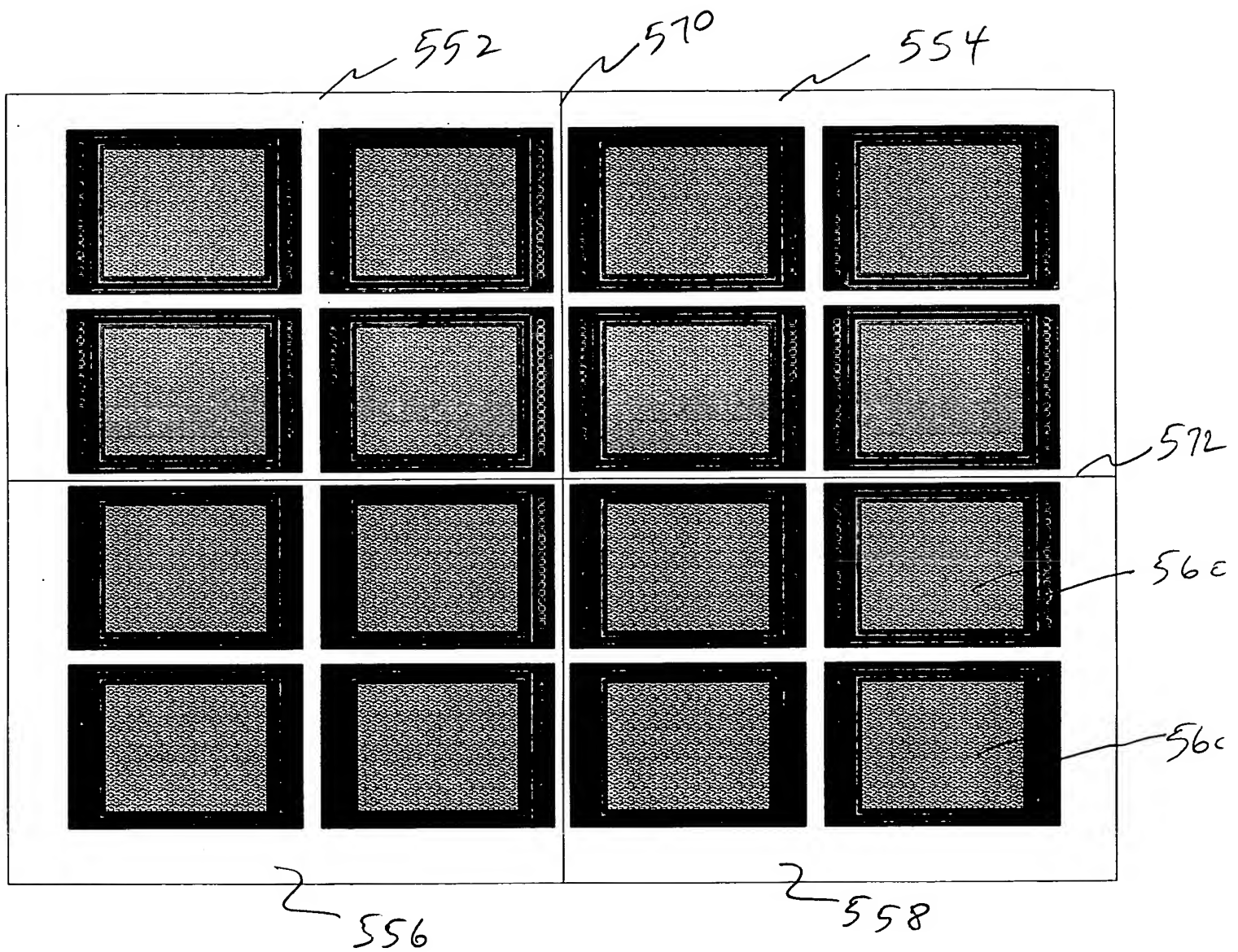
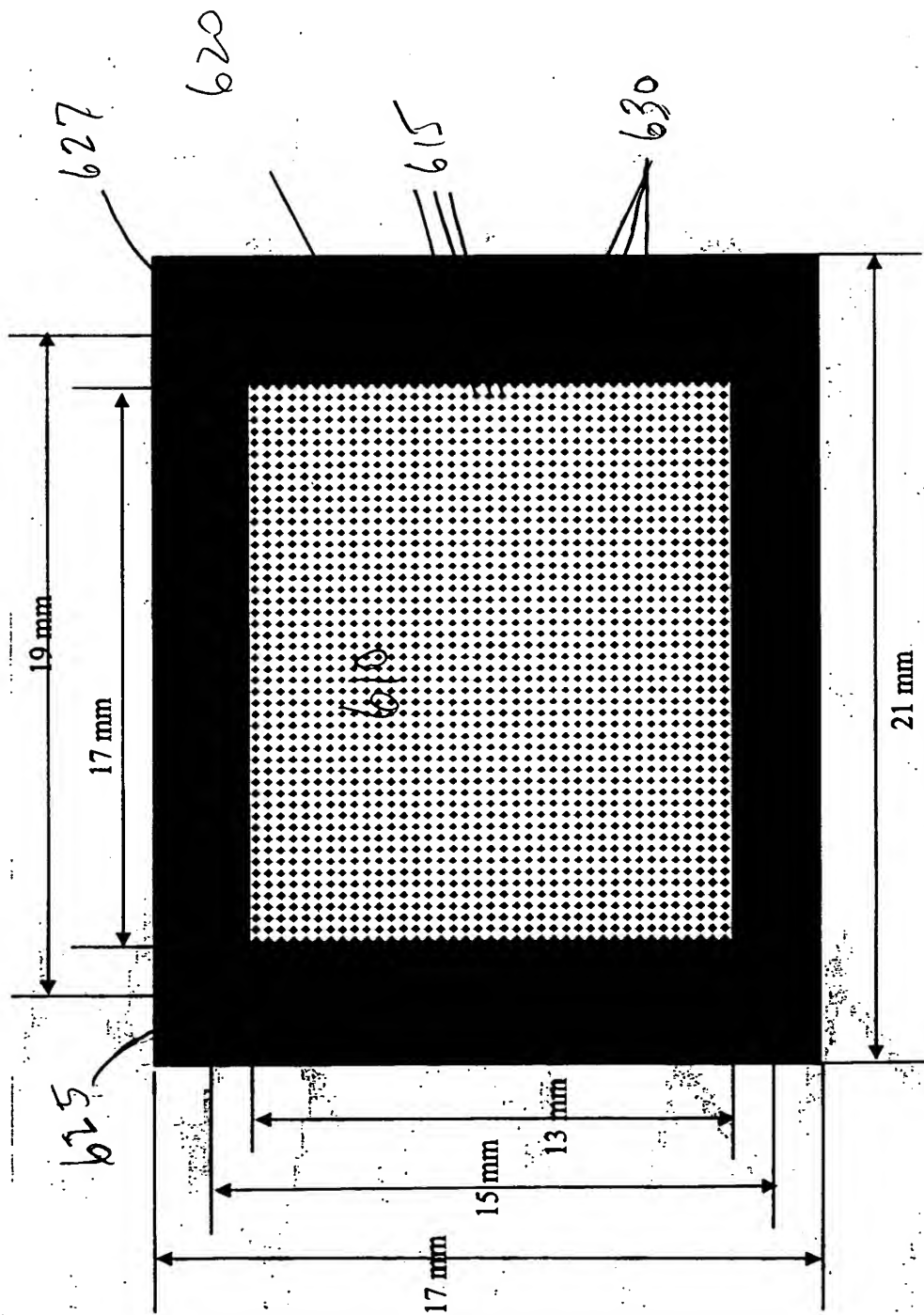


FIG. 5B

(FIG. 6)



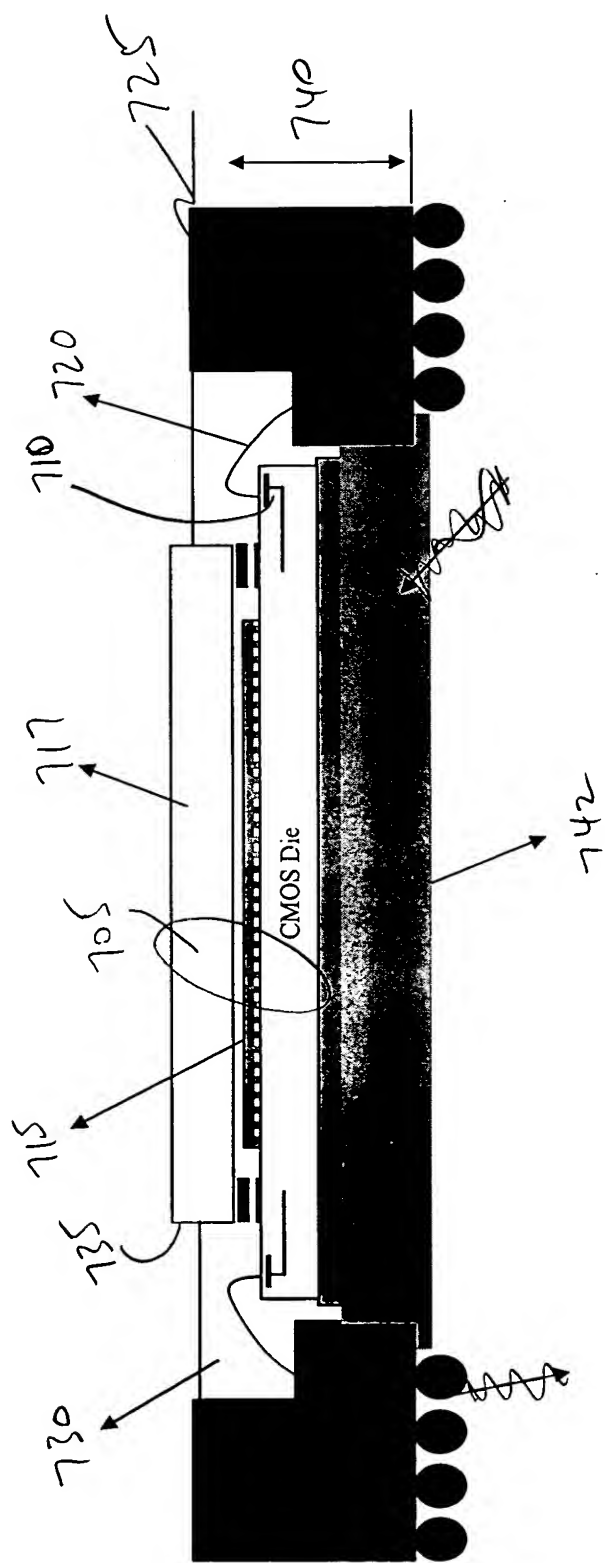


FIG. 7



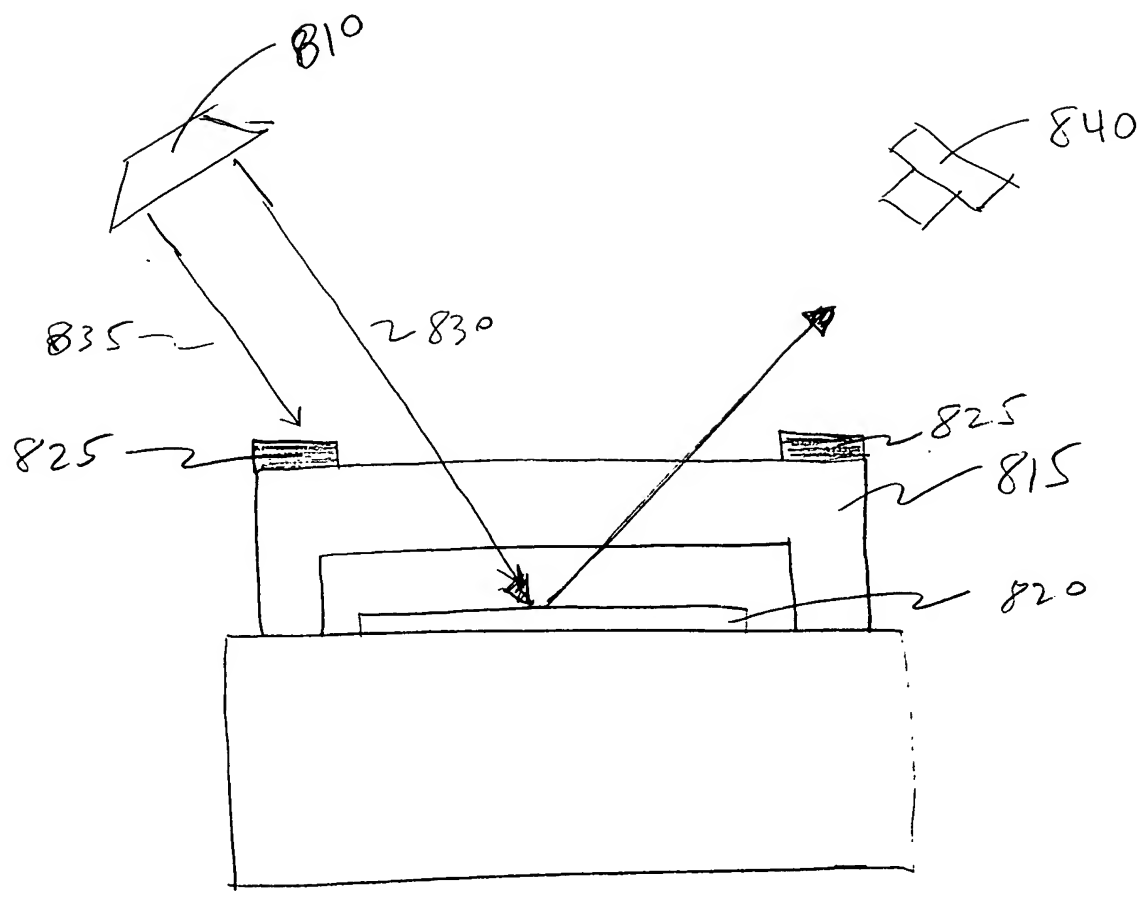


FIG. 8